

Technical Report

Worst-Case Communication Delay Analysis for Many-Cores using a Limited Migrative Model

Borislav Nikolic
Patrick Meumeu Yomsi
Stefan M. Petters

CISTER-TR-141002

2014

Worst-Case Communication Delay Analysis for Many-Cores using a Limited Migrative Model

Borislav Nikolic, Patrick Meumeu Yomsi, Stefan M. Petters

CISTER Research Center

Polytechnic Institute of Porto (ISEP-IPP)

Rua Dr. António Bernardino de Almeida, 431

4200-072 Porto

Portugal

Tel.: +351.22.8340509, Fax: +351.22.8340509

E-mail: borni@isep.ipp.pt, pamyo@isep.ipp.pt, smp@isep.ipp.pt

http://www.cister.isep.ipp.pt

Abstract

A steady increase in the number of cores within many-core platforms causes increasing contentions for the interconnect medium and leads to non-negligible latencies of intercore communication. In order to study the worst-case execution times of applications, it is no longer sufficient to only take into account schedulability requirements, but the communication delays also have to be considered. In this paper we focus on the worst-case communication delays of applications, assuming a Limited Migrative Model (LMM). LMM is an approach based on the multi-kernel paradigm - a promising step towards scalable and predictable many-cores. The contribution of this paper is threefold. First, we extend LMM by allowing inter-application communication, and adapt the existing worst-case communication delay analysis, to make it applicable to the enhanced model. Then, we propose a novel analysis. Finally, we compare these two methods. The experiments show that the new approach renders tighter upper-bound estimates in more than 90% of the cases, while demonstrating a comparable runtime performance.

Worst-Case Communication Delay Analysis for Many-Cores using a Limited Migrative Model

Borislav Nikolić, Patrick Meumeu Yomsi and Stefan M. Petters CISTER/INESC-TEC, ISEP, IPP, Porto, Portugal Email: {borni, pamyo, smp}@isep.ipp.pt

Abstract-A steady increase in the number of cores within many-core platforms causes increasing contentions for the interconnect medium and leads to non-negligible latencies of intercore communication. In order to study the worst-case execution times of applications, it is no longer sufficient to only take into account schedulability requirements, but the communication delays also have to be considered. In this paper we focus on the worst-case communication delays of applications, assuming a Limited Migrative Model (LMM). LMM is an approach based on the multi-kernel paradigm - a promising step towards scalable and predictable many-cores. The contribution of this paper is threefold. First, we extend *LMM* by allowing inter-application communication, and adapt the existing worst-case communication delay analysis, to make it applicable to the enhanced model. Then, we propose a novel analysis. Finally, we compare these two methods. The experiments show that the new approach renders tighter upper-bound estimates in more than 90% of the cases, while demonstrating a comparable runtime performance.

I. Introduction

The miniaturisation process in the semiconductor technology reached the stage where further processing power enhancements related to single-core systems are no longer affordable [1]. In order to satisfy the ever increasing demand for more powerful computational devices, chip manufacturers took a design paradigm shift [2], [3] and started interconnecting multiple cores within a single chip. Nowadays, platforms containing several cores (*multi-cores*) and more than a dozen of cores (*many-cores*) have become commonplace in many scientific areas, most notably *high performance computing*, while they are still an emerging technology in others, like *real-time embedded systems*. This paper focuses on the latter.

In many-cores, the *Network-on-Chip* (NoC [4]) architecture has become the predominant interconnect medium due to its scalability potential [5]. On such platforms the *wormhole switching* technique [6] is widely applied (e.g. [2], [3]) because of its good throughput and small buffering requirements [5].

In order to integrate many-cores in the real-time embedded domain, an OS paradigm is needed, such that it allows the system designer to exploit the full potential of the underlying hardware, while assuring predictability and scalability [7], [8],

This work was partially supported by National Funds through FCT (Portuguese Foundation for Science and Technology) and by ERDF (European Regional Development Fund) through COMPETE (Operational Programme 'Thematic Factors of Competitiveness'), within projects ref. FCOMP-01-0124-FEDER-037281 (CISTER), FCOMP-01-0124-FEDER-020536 (SMARTS), also by FCT and the EU ARTEMIS JU funding, within project ref. ARTEMIS/0003/2012, JU grant nr. 333053 (CONCERTO), also by the European Union, under the Seventh Framework Programme (FP7/2007-2013), grant agreement n° 611016 (P-SOCRATES), and by FCT and ESF (European Social Fund) through POPH (Portuguese Human Potential Operational Program), under PhD grant SFRH/BD/81087/2011.

which are essential prerequisites for deriving execution guarantees. The *Limited Migrative Model* (*LMM*) [9] is an approach based on the concepts of the *multi-kernel paradigm* [7], which is a novel OS design (e.g. [7], [10], [11]), and a promising step towards scalable and predictable many-cores.

Contribution: This paper focuses on the worst-case communication delays of applications, residing within a NoC-based, wormhole-switched many-core platform using LMM. Specifically, our aim is to derive, for each application, a tight upper-bound estimate on the duration of all communication it performs within its minimum inter-arrival period. First, we enhance LMM to include inter-application communication, and adapt the existing analysis [9], so as to make it applicable to the extended model. Then, we present a novel analysis. Finally, we compare these two approaches. The experiments show that the new method provides tighter upper-bounds and also demonstrates a comparable runtime performance.

II. LMM MOTIVATION AND CONCEPTS

Unpredictability, scalability, the need for load balancing and core shutdowns are some of the key issues that make the integration of many-cores into the real-time embedded domain a challenging subject. The existing state-of-the-art methodologies are not efficient in addressing all of the aforementioned challenges, and this significantly limits their scope of application, as highlighted below.

All approaches can be broadly classified into two categories: *Non-Migrative Approaches* and *Migrative Approaches*. In the scheduling theory, non-migrative approaches [12] are also known as *Fully-Partitioned Approaches*. Each application is migrationless, and is, at design time, statically assigned to a core where it has to execute. Migrative approaches are further divided into *Semi-Partitioned Approaches* and *Global Approaches*. The former category [13], [14] considers a static assignment of an application to a core (or a subset of cores if it migrates). A migrative application also follows design time decisions, i.e. always executes the prescribed fraction of work on each of the assigned cores. Conversely, in global approaches [15], [16] an application may execute on any core.

Evidently, fully partitioned and semi-partitioned approaches are inflexible. They can be very inefficient in scenarios with substantial load changes, where runtime load balancing and/or core shutdowns are required for energy/thermal management, performance enhancements or fault tolerance reasons. Conversely, global approaches inherently support load balancing. However, due to the necessity to maintain global structures (e.g. ready-queue) in a centralised entity, scalability issues arise [7], [8], and serious challenges occur when attempting real implementations [17].

LMM [9] is a recently introduced approach in the realtime embedded domain. Its novelty is twofold. First, it poses a constraint that each application may execute *only* on a subset of cores, which are decided at design time. During runtime, an application may migrate between the candidate cores. Second, the release/migration decisions of each application are made by the application itself, which removes the requirement of a mandatory centralised scheduling entity. This contributes to the scalability, and yet gives the possibility to perform runtime load balancing. So far, LMM appears to be a promising approach, and this work is motivated by that reasoning. More details about LMM are given in Section IV-B.

III. RELATED WORK

Contrary to the popular belief, the wormhole switching technique [6] is not a novelty neither in academia, nor in industry. However, it had been largely neglected because an alternative *store-and-forward switching* was providing satisfactory results [5]. But as the amount of transferred data kept increasing, the buffering within routers became a challenge [5]. This brought the wormhole switching back into focus, and some present many-cores employ this technique, e.g. [2], [3].

When organising the access to the interconnect medium, platforms employ different techniques, which can be classified as either contentionless or contention aware. An example of the former category is the Aethereal platform [18] where a time-division-multiple-access method is used to organise the access to the interconnect resources. If contentions are allowed and a platform provides only a single virtual channel, complex traffic interference patterns may occur [19]. Several analyses were proposed to obtain upper-bounds on the worst-case delays [20]–[23], however, due to the complex interference patterns, the obtained values may be overly pessimistic [20].

Conversely, if multiple virtual channels are employed [24], [25], two benefits arise: (i) the throughput can significantly increase [24], [25] and (ii) traffic preemptions can be implemented [26]. Shi and Burns [27] proposed the worst-case delay analysis, assuming per-priority virtual channels, flit-level preemptions and per-packet distinctive priorities.

The aforementioned approaches are based on the assumption that all traffic routes are known at design time. Yet, LMM causes non-deterministic traffic routes (Section VI-A), which renders these approaches not suitable to our model. Nikolić and Petters [9] recognised this problem and proposed a naive and simplistic method to compute the worst-case communication delays. The limitations of this method are: (i) it considers only intra-application traffic and (ii) it treats path non-determinism in a pessimistic way, by assuming that each application may suffer interference from all higher priority traffic that exists within the network, irrespective of (im)possible contentions. In this paper we tackle the traffic non-determinism via new techniques, and propose a novel worst-case communication delay analysis.

IV. MODEL

A. Platform

We consider a contention-aware NoC-based many-core platform, comprised of $m \times n$ tiles interconnected with a 2D mesh, where each tile contains a single core and a single router,

e.g. Tilera family of processors [3]. The platform employs a static, dimension-ordered XY routing mechanism, which is deadlock and livelock free [28]. With this policy, packets firstly travel on the x-axis, and upon reaching the x-coordinate of the destination, continue along the y-axis. Moreover, the data transfer is implemented via a wormhole switching technique with the flow-control mechanism, where acknowledgements use separate physical links. This means that, prior to sending, each data packet is divided into small elements of fixed size called flits. The first flit establishes the path, and the rest follow in a pipelined manner. Additionally, the target platform provides virtual channels, which are implemented as additional flit-sized buffers within each port of each router. A virtual channel may be used to store one flit of a preempted traffic packet. A prerequisite is that the number of virtual channels is at least equal to the maximum number of contentions at any router. This requirement assures that each packet has an available virtual channel within each port along its path, which is a realistic assumption [29]. Indeed, on a 10×10 grid, the workload of 400 packets would require on average only 8 virtual channels [29]. In this paper, the terms packet and message are used interchangeably.

B. Software Layers

LMM builds on top of a multi-kernel paradigm, meaning that each core runs an independent kernel instance. Kernels communicate among each other and constitute the basic communication infrastructure. Each kernel exposes some of its functionalities to applications located on its core via system calls. In order to interact with other applications residing on the same or other cores, applications invoke available system calls. Each kernel performs a local scheduling on its core.

An application may execute only on a subset of cores, which are decided at design time. On each of the selected cores the execution code of that application exists, constituting an entity called *dispatcher*. Dispatchers of the same application (each located on a different core) communicate and decide whether the migration will occur, and if so, which core (dispatcher) is the destination. The aforementioned communication is called *agreement protocol* [9], and its purpose is to elect one dispatcher (*master dispatcher*), which will subsequently release on its core the next job, on behalf of the entire application. The job has to complete on the master's core, it cannot migrate (no job-level migrations). Applications are single-threaded and implemented as recurrent tasks, therefore at any time instant there can be only one master per application.

Once the job execution completes, the master is responsible to start the agreement protocol again. Other dispatchers that participate in the communication are called *slave dispatchers*. If the outcome of the protocol is that the migration occurs, the previous master becomes the slave, while the newly elected dispatcher becomes the master. Additionally, the execution context has to be transferred from the old to the new master, so that the new master can release the next job on its core.

The protocol execution is termed *intra-application com-munication*. Perceived from the application's perspective, its dispatchers exchange one master token. Thus, a master is only a temporary role of a dispatcher. We call this property *master volatility*, and it has several implications which are covered in Section VI-A. Figure 1 gives a graphical representation of

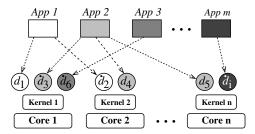


Fig. 1: Limited migrative model - LMM

LMM, where the current master dispatchers are emphasized by dots over their respective names. Notice, that in LMM each core performs a local scheduling (similar to fully partitioned approaches), while each application still has a possibility to migrate (similar to global approaches). These benefits come at a price of protocol communication, and the more migrative flexibility the applications have (the number of dispatchers), the more substantial are the communication delays (see Figure 2). The purpose of this example is to help the reader perceive the notion of LMM and its scalability potential. The simulation parameters are given in Table I (Section VII).

Note, the existing definition of LMM and the accompanying analysis [9] only consider intra-application traffic, i.e. agreement protocols. In this work we extend LMM and allow applications to communicate with each other, e.g. for synchronisation or data-sharing purposes. We term this process inter-application communication, and it is performed by an exchange of messages between current master dispatchers of interacting applications. We also extend the existing analysis [9] (Section VI-B), so as to match the enhanced model.

The execution workload is described by an application-set $\mathcal{A} \stackrel{\mathrm{def}}{=} \{a_1, a_2, ..., a_{m-1}, a_m\}$. Each application $a\langle C_a, T_a, P_a, \mathcal{D}_a, \mathcal{M}_a \rangle$ is characterised by its execution time C_a , a minimum inter-arrival period T_a , a unique priority P_a , a set of dispatchers \mathcal{D}_a and a set of messages $\mathcal{M}_a = \mathcal{M}_I(a) \cup \mathcal{M}_S(a) \cup \mathcal{M}_R(a)$, where $\mathcal{M}_I(a)$ is a set of intra-application messages, while $\mathcal{M}_S(a)$ and $\mathcal{M}_R(a)$ represent sets of sent and received inter-application messages, respectively. Every message m_{ij} from the source dispatcher d_i to the destination dispatcher d_j is characterised by (i) a priority it inherits from the application of the sender dispatcher d_i , (ii) the amount of the transferred content - $size(m_{ij})$ and (iii) a traversed path - $path(m_{ij})$, consisting of $nhops(m_{ij})$ hops. If both d_i and d_j belong to the same application a, then m_{ij} is an intra-application message, i.e. $m_{ij} \in \mathcal{M}_I(a)$. Otherwise, if $d_i \in \mathcal{D}_a$ and $d_j \in \mathcal{D}_{a'}$, it is an inter-application message, i.e. $m_{ij} \in \mathcal{M}_S(a) \land m_{ij} \in \mathcal{M}_R(a')$.

V. BACKGROUND AND PRELIMINARIES

Before we present the main contribution of this work, let us introduce several basic properties of the NoC contention analysis, which are important for understanding our approach.

Prop. 1: An isolation delay of a message is equal to the latency of its first flit to reach the destination, augmented by the processing time of all flits at the destination router (Equation 1). l_{sw} is the latency of the arbitration and the crossbar switching, l_t is the latency to transfer one flit between two successive routers, while size(f) is the size of the flit.

$$l(m) = nhops(m) \times (l_{sw} + l_t) + \left\lceil \frac{size(m)}{size(f)} \right\rceil \times l_t$$
 (1)

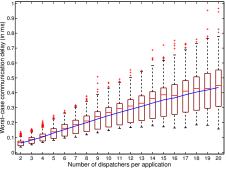


Fig. 2: The impact of the number of dispatchers on the communication delay

Prop. 2: Due to flit-level preemptions, a message can be blocked by the lower-priority traffic, at most for the duration of one flit traversal, within each router on its path (Equation 2).

$$b(m) = nhops(m) \times (l_{sw} + l_t) \tag{2}$$

Prop. 3: A message m of an application a can be preempted only by higher priority messages which share a part of the path with it, called *directly interfering messages*. Let $\mathcal{M}_D(m)$ be a set of directly interfering messages of m. Formally:

$$\forall m' \in \mathcal{M}_{a'} : \left(P_{a'} > P_a \land path(m') \cap path(m) \neq \emptyset\right) \Rightarrow m' \in \mathcal{M}_D(m) \tag{3}$$

Prop. 4: Interference caused by a single preemption of any $m' \in \mathcal{M}_D(m)$ to m is equal to the sum of its isolation and blocking delays -l(m') + b(m').

Note, in scenarios with a single virtual channel, a message might suffer interference from other higher priority messages which do not directly interfere with it, i.e. do not share any part of the path with it [19], [27]. However, when assuming per-priority virtual channels (as in this work), such messages cannot cause interference, but can influence the occurrence patterns of directly interfering messages [27], [30]. Thus, assuming periodic occurrences of directly interfering messages is an unsafe assumption, and in the analysis we have to consider their worst-case occurrence patterns. This is a well-known issue in the wormhole switching and a detailed explanation can be found in the following works [27], [30].

VI. PROPOSED APPROACH

A. Challenges of LMM

Due to the master volatility in LMM (i.e. the ability of every dispatcher to become a master), it is not possible to predict at design time which dispatchers will be elected masters of their applications at a particular time instant at runtime. Furthermore, as each master initiates the agreement protocol and performs the inter-application communication on behalf of the entire application, message paths will highly depend on which dispatcher performs that role. The leftmost part of Figure 3 shows agreement protocol messages of one application, captured at two different time instants. Depending on which dispatcher is the current master (emphasized circle), messages may traverse completely different paths. A similar problem occurs when analysing the inter-application traffic. As every dispatcher of both interacting applications can be a master, a single inter-application message may take any of the routes given in the left part of Figure 4. It is evident that non-deterministic message paths render traditional worst-case communication delay analyses inapplicable to LMM.

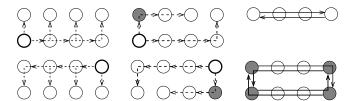


Fig. 3: Intra-Application Communication

B. Extension of the Existing Approach [9]

As already discussed, Nikolić and Petters [9] proposed the analysis for LMM, assuming only intra-application traffic. Their approach is path-abstracting and circumvents the problem of non-deterministic traffic routes. Specifically, the analysis exploits the fact that isolation and blocking delays of a message (Equations 1-2) are the consequence of 2 properties: (i) its size -size(m) and (ii) its path length -nhops(m). The size of the message is deterministic, but the path is not. Let maxhops(a) be the maximum distance between any two dispatchers of one application. As maxhops(a) is an upperbound on the path length of each intra-application message m, the analysis covers the worst-case by assuming that each m traverses that distance, $\forall m \in \mathcal{M}_a : nhops(m) = maxhops(a)$. Furthermore, it is assumed that any higher priority message (i) will also traverse its longest possible path and (ii) will cause interference, irrespective of its potential route. Subsequently, this concept was applied to the entire traffic.

Now we extend that reasoning and apply the same approach to inter-application traffic. Let a and a' be two interacting applications with the maximum distance between the furthest dispatchers equal to maxhops(a,a'). Consequently, each inter-application message m between a and a' will traverse at most that distance, so nhops(m) = maxhops(a,a') covers the worst-case. Similarly, the conservative assumption is that any higher priority inter-application message (i) will traverse its longest possible path and (ii) will cause interference.

The isolation and blocking delays of an application, $del_I(a)$ and $del_B(a)$ are equal to the sum of isolation and blocking delays of its intra-application messages and sent inter-application messages, respectively (Equations 4-5).

$$del_{I}(a) = \sum_{\forall m \in \mathcal{M}_{I}(a) \cup \mathcal{M}_{S}(a)} l(m) \quad del_{B}(a) = \sum_{\forall m \in \mathcal{M}_{I}(a) \cup \mathcal{M}_{S}(a)} b(m)$$
(5)

In order to consider the worst-case interference, we have to compute the maximum traffic that every higher priority application a' can generate within the observed time interval. For that, we use Theorem 1, taken from the existing analysis [9].

Theorem 1. [9] The number of protocol executions of any application a within the time interval t can be at most $1 + \left\lfloor \frac{t-C_a}{T_a} \right\rfloor$.

Given that for each application the protocol is performed once per its inter-arrival period, the result of Theorem 1 also presents the maximum number of inter-arrivals of a given application. Therefore, the maximum interference suffered from all higher priority applications within one inter-arrival period T_a , termed $del_N(a)$, can be computed by multiplying the maximum number of their inter-arrivals with the maximum traffic they can generate during one inter-arrival period (Equation 6).

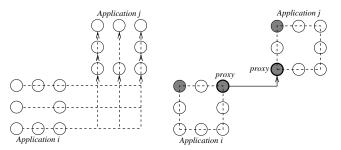


Fig. 4: Inter-Application Communication

$$del_{N}(a) = \sum_{\forall a' \in \mathcal{A}: P_{a'} > P_{a}} \left(1 + \left\lceil \frac{T_{a} - C_{a'}}{T_{a'}} \right\rceil \right) \times \left(del_{I}(a') + del_{B}(a') \right) \ \ (6)$$

Note, a message can be stalled by other same-priority messages of the same application with which it shares the same virtual channel. A safe assumption is that all messages of the same application traverse sequentially and hence each of them contributes to the isolation delay of the application with its entire traversal latency (see Equation 4). The same reasoning will be used in the novel approach (Section VI-C).

The worst-case communication delay of an application is equal to the sum of these terms:

$$del(a) = del_I(a) + del_B(a) + del_N(a)$$
(7)

C. Novel approach

Main idea: Recent insights into priority-preemptive, wormhole-switched NoCs showed that the dominant factor in the worst-case delay of a message is not the length of its path, but rather the interference it suffers [29]. Thus, the pessimism related to the approach presented in the previous section can be attributed mostly to the conservative assumption that all higher priority traffic can cause interference, irrespective of (im)possible contentions. Motivated by this reasoning, we propose a novel approach, which relies on enforcing constraints, in order to make LMM traffic deterministic and predictable.

Constraint 1. Dispatchers of an application can be positioned only on the edges of a rectangular $x \times y$ structure, such that no corner is left unoccupied and $x, y \in \mathbb{N}$. The special case is a line-like shape, where one or both dimensions of the shape are equal to "1".

Constraint 2. Intra-application messages travel only on the edges of the shape its application forms, and re-routing occurs where needed to comply with the global XY routing policy. An individual message rotation (i.e. clockwise or counterclockwise) is chosen such that the traversal distance is minimised.

The middle part of Figure 3 illustrates how dispatchers should be placed and messages consequently routed. Shaded dispatchers denote locations where reroutings occur. Reroutings are fast on-core routines, performed in an interrupt-like manner which can be, for example, implemented by instrumenting the HardwallTM technology of Tilera platforms [3].

1) **Supermessages and Proxies:** Constraints 1-2 make the part of the network that intra-application traffic uses deterministic. To make the traffic master independent as well, we introduce the following concepts: *supermessages* and *proxies*.

Definition 1 (Supermessage). A supermessage is a message which connects (i) diagonally-placed dispatchers if an application has a rectangular shape, or (ii) terminal dispatchers if an application has a line-like shape.

An application with a line-like shape yields 2 supermessages $-\widehat{m}_{l1}$, \widehat{m}_{l2} , and does not involve reroutings, while an application with a rectangular shape has 4 supermessages, of which 2 with clockwise orientation \widehat{m}_{cv1} , \widehat{m}_{cv2} , and 2 with counter-clockwise orientation \widehat{m}_{cc1} , \widehat{m}_{cc2} (see the right part of Figure 3). Due to space limitations, in the rest of this work only rectangular shapes are analysed. Indeed, any conclusion reached for a rectangular shape can be applied to a line-like shape by considering only one supermessage of each orientation and treating the other one as non-existent (i.e. $\widehat{m}_{l1} = \widehat{m}_{cv1}$; $\widehat{m}_{l2} = \widehat{m}_{cc1}$; $\widehat{m}_{cv2} = \emptyset$; $\widehat{m}_{cc2} = \emptyset$;).

Theorem 2. Any intra-application message of an application can be expressed by at most 2 distinctive, same-orientation supermessages, with at most 1 rerouting.

Proof: Proven by contradiction. An intra-application message m assumes the orientation such that the distance between the dispatchers is minimised (Constraint 2). If σ is the circumference of an application shape, it holds that $nhops(m) \leq \frac{\sigma}{2}$. As each supermessage \widehat{m} connects diagonal corners of a shape, for every \widehat{m} it holds that $nhops(\widehat{m}) = \frac{\sigma}{2}$.

Assume that m can be expressed with at least 3 same-orientation supermessages. Note, as there are only two same-orientation supermessages (e.g. $\widehat{m_1}$ and $\widehat{m_2}$), one of them has to appear twice, i.e. the sequence would be $\{\widehat{m_1}, \widehat{m_2}, \widehat{m_1}\}$, or $\{\widehat{m_2}, \widehat{m_1}, \widehat{m_2}\}$. In either case, the middle supermessage entirely belongs to m, while the first and the last belong with fractions $\epsilon_1 > 0$ and $\epsilon_2 > 0$, respectively. Hence:

$$nhops(m) = \epsilon_1 + nhops(\widehat{m_1}) + \epsilon_2 = \epsilon_1 + nhops(\widehat{m_2}) + \epsilon_2 = \epsilon_1 + \frac{\sigma}{2} + \epsilon_2 > \frac{\sigma}{2}$$

A contradiction has been reached. Additionally, as reroutings occur only on places where supermessages meet, and since any message can be expressed by at most 2 distinctive supermessages, it can involve at most 1 rerouting.

Notice, that supermessages are master-independent and their number is significantly smaller than the number of possible message paths. Thus, the intuitive idea behind our novel approach is to transform every intra-application message into the corresponding supermessage(s) with eventual rerouting, and perform the analysis on such a model.

We apply a similar approach to inter-application traffic:

Definition 2 (Proxy). A proxy dispatcher is a dispatcher which is selected at design time, and which participates in the inter-application communication. It mediates in the message exchange between its master and the proxy dispatcher of the other (interacting) application.

An illustrative example of Definition 2 is given in the right part of Figure 4. In this scenario an inter-application message is divided into 5 different components: 1) a message from the master sender to its proxy, 2) a rerouting on the core of the proxy sender, 3) a message between the proxies, 4) a rerouting on the core of the proxy receiver, 5) a message from the proxy receiver to its master. Proxies are decided at

design time, thus a message m_{ij} between proxy dispatchers d_i and d_j is also deterministic and master-independent. An application can have multiple proxies, each responsible for the communication with a different application. If a proxy receives an inter-application message during the agreement protocol of its application, the message is stalled inside the proxy until the protocol completes.

Theorem 3. Any inter-application message can be expressed by (i) at most 2 distinctive, same-orientation supermessages on the sender's side, (ii) a message between a proxy sender and a proxy receiver, (iii) at most 2 distinctive, same orientation supermessages on the receiver's side and (iv) at most 4 reroutings.

Proof: Proven directly. A message between a master sender and its proxy complies with the rules of the intraapplication traffic, hence, according to Theorem 2, it can be expressed by at most 2 distinctive same-orientation supermessages. The same conclusion holds for the message between a receiver proxy and its master. A message between proxies is a non-constrained point-to-point message. Additionally, messages between masters and their proxies on both sender and receiver side each yield at most 1 rerouting (Theorem 2). Finally, an inter-proxy message causes 1 rerouting on the core of a proxy sender and 1 on the core of a proxy receiver.

By introducing placement and rerouting constraints, supermessages and proxies, we consciously made a decision to potentially "sacrifice" performance (i.e. messages may traverse longer distances and involve reroutings). Nonetheless, this approach causes predictable and deterministic message paths, which allows us to perform a more detailed and less pessimistic analysis (covered later in Section VI-D), and derive tighter worst-case communication delay estimates.

According to Theorems 2-3, all intra- and inter-application traffic of all applications can be expressed with (i) a set of supermessages $-\widehat{\mathcal{M}}$, (ii) a set of proxy-to-proxy messages $-\mathcal{M}_P$, and (iii) a set of reroutings $-\mathcal{R}$, which are all master-independent and known at design time. In order to be able to perform the analysis, the maximum number of occurrences O(m) of each message $m \in (\widehat{\mathcal{M}} \cup \mathcal{M}_P)$ within a minimum inter-arrival period of its application has to be computed. Also, the maximum number of rerouting occurrences $r(d) \in \mathcal{R}$, caused by each dispatcher d on its core within the same interval has to be computed.

- 2) Finding the maximum number of occurrences: As is evident from the previous section, the maximum number of occurrences of both supermessages and reroutings of each application depends on (i) the employed agreement protocol and (ii) the amount of its inter-application traffic. So far, 3 agreement protocols have been proposed (*Master-Slave*, *List* and *Hybrid*) [9]. Since the first one suffers from race conditions, in this paper we focus only on the second and third.
- List protocol [9] is an opportunistic agreement protocol; if a master can continue the execution it does so, without initiating the communication. Otherwise it sends a request to the first neighbouring slave assuming a clockwise orientation. The slave tries to accommodate the execution of the next job instance. If it can, it signals to the old master, receives a context

and becomes the new master. Otherwise it passes a request to the next neighbouring slave, also assuming a clockwise orientation. The process repeats until one dispatcher announces the possibility to execute the next job and consequently becomes a new master. The worst-case scenario occurs when the job can be accommodated only by the last traversed dispatcher, and it has to be covered¹. The downside of this protocol is that a selective scheduling policy is not easily implemented, i.e. the first dispatcher that can execute the job will do so, while maybe better candidates exist within non-traversed dispatchers.

By applying Theorem 2 to every message of the List protocol, we can analytically express the entire protocol as a function of supermessages and reroutings. Subsequently, we can compute the isolation delay $del_I(List)$, the blocking delay $del_B(List)$ and the rerouting delay $del_R(List)$ of one protocol execution. Furthermore, we can obtain the maximum number of occurrences of each supermessage \widehat{m} during one protocol execution $O_P(\widehat{m})$ and during one context transfer $O_C(\widehat{m})$. Finally we can compute the maximum number of reroutings $r_P(d)$ occurring on the core of a dispatcher d, caused by one protocol execution of its application. Due to space limitations, the analytical steps on how to obtain these values are given in the technical report [32].

• Hybrid protocol [9] is the agreement protocol which consists of two phases. The first phase is initiated when a current master sends a broadcast towards all slave dispatchers. In this way a master requests the information from all slaves about how likely it is that they can accommodate the execution of the next job on their cores. The requests may require the information regarding core utilisation, core temperature, schedulability, etc.² Upon receiving the request, each slave queries its kernel for requested information. When the kernel replies, the slave sends the response back to the master. The end of the first phase is reached when the master receives all responses. Then, the master makes an ordered list where all dispatchers are sorted based on the likelihood of accommodating the execution of the next job. If the master is on the top of the list, it continues to be the master. Otherwise, the second phase begins. It is similar to the list protocol, i.e. the master sends a message towards the first slave. However, in this case not to the nearest neighbour in the clockwise orientation, but to the first dispatcher of the generated list. If the slave can accommodate the execution, it requests the context from the master. If not, it passes the request to the next dispatcher from the list. The process repeats until one dispatcher informs the master that it can schedule the job³. The worst-case scenario occurs when only the last dispatcher of the ordered list is able to do so. Finally, the master transfers the context to the newly elected slave, which becomes the new master.

Similar to the List protocol, Theorem 2 can be applied to every message of the Hybrid protocol, and the entire protocol can also be analytically expressed as a function of supermessages and reroutings. By doing so, we can obtain the isolation delay $del_I(Hyb)$, the blocking delay $del_B(Hyb)$ and the rerouting delay $del_R(Hyb)$ of one protocol execution. Additionally, we can compute the maximum number of occurrences of each supermessage \widehat{m} during one protocol execution $O_P(\widehat{m})$ and during one context transfer $O_C(\widehat{m})$. Finally we can compute the maximum number of reroutings $r_P(d)$ occurring on the core of a dispatcher d, caused by one protocol execution of its application. The analytical steps on how to obtain these values are given in the technical report [32].

• Furthermore, each application may perform interapplication communication with other applications. Recall (Theorem 3), that any inter-application message can be expressed as a function of (i) sender's supermessages, (ii) receiver's supermessages, (iii) a proxy-to-proxy message and (iv) reroutings. Thus, by applying Theorem 3 to every interapplication message of one application, we can analytically describe its entire inter-application traffic as a function of the aforementioned constructs. Subsequently, we can obtain the isolation delay $del_I(Send)$, the blocking delay $del_B(Send)$ and the rerouting delay $del_R(Send)$ of the inter-application traffic that an application sends. In the same way, we can obtain the isolation delay $del_I(Rcv)$, the blocking delay $del_B(Rcv)$ and the rerouting delay $del_B(Rcv)$ of the interapplication traffic that an application receives. Additionally, for each supermessage \widehat{m} and inter-proxy message m_{proxy} the maximum number of occurrences as a consequence of the inter-application traffic can be obtained – $O_I(\widehat{m})$ and $O_I(m_{proxy})$, respectively. Finally, the maximum number of reroutings $r_I(d)$ that a dispatcher d induces during the interapplication communication of its application can be computed. The analytical steps on how to obtain these values are given in the technical report [32].

In summary, between any two consecutive job releases of an application, its supermessage \widehat{m} can appear $O_P(\widehat{m})$ times during the protocol execution, $O_C(\widehat{m})$ times during the context transfer and $O_I(\widehat{m})$ times for every sent and received inter-application message. Furthermore, each proxyto-proxy message m_{proxy} may appear $O_I(m_{proxy})$ times due to inter-application traffic. Additionally, the maximum number of reroutings that each dispatcher d induces is equal to the sum of reroutings during a protocol execution and during interapplication communication $-r(d)=r_P(d)+r_I(d)$.

D. Performing the Analysis

Let a be the application under analysis. Its worst-case communication delay (Equation 8), consists of several terms, namely, the isolation delay $del_I(a)$, the blocking delay $del_B(a)$, the rerouting delay $del_R(a)$, the network interference $del_{NI}(a)$ and the rerouting interference $del_{RI}(a)$. Now we describe how to compute the individual terms of Equation 8.

$$del(a) = del_I(a) + del_B(a) + del_R(a) + del_{NI}(a) + del_{RI}(a)$$
(8)

• The **isolation delay** (Equation 9). It is equal to the sum of isolation latencies of (i) the protocol execution, (ii) sent interapplication messages and (iii) received inter-application messages. Depending on the agreement protocol of the application under analysis $del_I(Protocol(a)) \in \{del_I(List), del_I(Hyb)\}$.

 $^{^{1}}$ In this work we assume that, at any time instant, at least one of the dispatchers will be able to accommodate the execution of the next job. Providing such guarantees falls into the domain of LMM schedulability analysis, and it is covered in our work [31].

 $^{^2}$ The policy of the agreement protocol depends on the specific purpose of the system and is immaterial for the discussion in this paper, since we study protocols only as an infrastructure to allow load balancing under LMM.

³See Footnote 1

• The blocking delay (Equation 10) and the rerouting **delay** (Equation 11). They are equal to the sum of blocking and rerouting latencies of (i) the protocol execution and (ii) inter-application traffic. Depending on the selected agreement protocol, $del_B(Protocol(a)) \in \{del_B(List), del_B(Hyb)\},\$ and $del_R(Protocol(a)) \in \{del_R(List), del_R(Hyb)\}.$

$$del_B(a) = del_B(Protocol(a)) + del_B(Snd) + del_B(Rcv)$$
 (10)

$$del_R(a) = del_R(Protocol(a)) + del_R(Snd) + del_R(Rcv)$$
 (11)

• The **network interference** (Equation 15). First, let us discuss the interference that a higher priority supermessage \widehat{m} of an application $a' \neq a$ can cause to a, between any two consecutive job releases of a' (Equation 12). It is equal to the sum of isolation and blocking latencies (i) while performing the protocol, (ii) while transferring the context and (iii) while performing the inter-application communication.

$$\delta(\widehat{m}) = \overbrace{O_P(\widehat{m}) \times (l_P(\widehat{m}) + b(\widehat{m}))}^{\text{protocol}} + \overbrace{O_C(\widehat{m}) \times (l_C(\widehat{m}) + b(\widehat{m}))}^{\text{context}} +$$

inter-application traffic
$$\sum_{\forall m \in \mathcal{M}_S(a') \cup \mathcal{M}_R(a')} O_I(\widehat{m}) \times (l_I(\widehat{m}) + b(\widehat{m}))$$
 (12)

Notice, that context transfers and inter-application messages may have different sizes than protocol messages, hence their occurrences and traversal latencies have to be computed separately, i.e. $l_P(\widehat{m}) \neq l_C(\widehat{m}) \wedge l_P(\widehat{m}) \neq l_I(\widehat{m})$. Conversely, the blocking delay $b(\hat{m})$ is not dependent on the message size, but is constant for all occurrences of the supermessage (Equation 2). Thus, $b_P(\widehat{m}) = b_C(\widehat{m}) = b_I(\widehat{m}) = b(\widehat{m})$.

Second, let us discuss the interference that a higher priority inter-proxy message m_{proxy} of an application $a' \neq a$ can cause to a, between any two consecutive job releases of a'(Equation 13). Recall, inter-proxy messages appear only in the inter-application traffic and not in protocols.

$$\delta(m_{proxy}) = O_I(m_{proxy}) \times (l(m_{proxy}) + b(m_{proxy})) \tag{13}$$

Once we have obtained interference delays that any higher priority supermessage and any higher priority inter-proxy message might cause (Equations 12-13), we can compute the total network interference $del_{NI}(a)$ that a suffers during its minimum inter-arrival period. As paths of all messages are deterministic and known at design time, we can take that knowledge into account when performing the analysis. Recall, that in Section V we defined a set $\mathcal{M}_D(m)$ of directly interfering messages of a message m. Those are higher priority messages that share a part of the path with m and hence can preempt it and cause interference. Similarly, here we define a set of directly interfering messages, but of an entire application a, termed $\mathcal{M}_D(a)$. A message belongs to the set $\mathcal{M}_D(a)$ if it is a directly interfering message of any message $m \in (\widehat{\mathcal{M}(a)} \cup \mathcal{M}_P(a))$, where $\widehat{\mathcal{M}(a)}$ is the set of supermessages of a and $\mathcal{M}_P(a)$ is the set of inter-proxy messages of a. Formally:

$$\forall m' : \exists m \in \left(\widehat{\mathcal{M}(a)} \cup \mathcal{M}_P(a)\right) \land m' \in \mathcal{M}_D(m) \Rightarrow m' \in \mathcal{M}_D(a) \quad (14)$$

Every message $m' \in \mathcal{M}_D(a)$ can cause interference to a. Thus, the maximum network interference that a can suffer, from all its directly interfering messages, is computed by summing up individual interferences that each message $m' \in \mathcal{M}_D(a)$ can cause (Equation 15). Individual terms are obtained by multiplying the maximum interference that m'can induce between any two consecutive job releases of its application a' (computed either by Equation 12 if m' is a supermessage, or by Equation 13 if m' is a proxy message), with the maximum number of inter-arrivals of a'.

$$del_{NI}(a) = \sum_{\forall m' \in \mathcal{M}_D(a)} \left(1 + \left\lceil \frac{T_a - C_{a'}}{T_{a'}} \right\rceil \right) \times \delta(m')$$
 (15)

• The **rerouting interference** (Equation 17). This is the interference that an application suffers while performing its rerouting operations. Notice that an application can suffer rerouting interference only on cores (tiles) where itself has dispatchers that perform rerouting operations. Let $\mathcal{D}_R(a)$ be a set of dispatchers of a which induce at least one rerouting operation on their respective cores. Formally:

$$\forall d \in \mathcal{D}_a : r_P(d) \neq 0 \lor r_I(d) \neq 0 \Rightarrow d \in \mathcal{D}_R(a)$$
 (16)

The routing interference of a (Equation 17) is computed by summing up individual interferences that each on-core dispatcher d' might cause to each dispatcher $d \in \mathcal{D}_R(a)$.

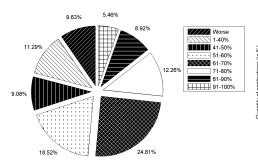
$$del_{RI}(a) = \sum_{\forall d \in \mathcal{D}_{R}(a)} \sum_{\forall d' \in c(d)} \left(1 + \left\lceil \frac{T_{a} - C_{a'}}{T_{a'}} \right\rceil \right) \times (r_{P}(d') + r_{I}(d')) \times l_{r}$$

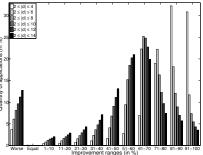
$$(17)$$

c(d) represents the core of a dispatcher d, and l_r denotes the latency of a single rerouting operation. Individual terms are obtained by multiplying the maximum interference that d' can cause between any two consecutive job releases of its application a' with the number of its inter-arrivals.

E. Discussion

The extended version of the previous method, presented in Section VI-B, does not involve reroutings, and hence does not have rerouting-related components $del_R(a)$ and $del_{RI}(a)$ (see Equation 7). However, the limitation of this approach is that it treats the path non-determinism in a pessimistic manner and computes the interference on the application-level (see Equation 6). Conversely, the newly proposed analysis employs application placement and rerouting constraints, in order to express the traffic as a function of supermessages, inter-proxy messages and reroutings. This strategy imposes longer message distances and employs a rerouting mechanism, which both additionally contribute to the worst-case communication delay. However, this approach makes message paths deterministic and known at design time. Consequently, the interference component can be computed with much less pessimism and hence tighter upper-bounds on the entire worst-case communication delay can be obtained. We back up this claim with the fact that the recent insights into the priority-preemptive wormhole-switched NoCs [29] showed that the most dominant factor in the worst-case communication delay analysis is the interference component, hence deriving its upper-bound as tight as possible is very important.





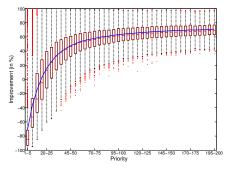


Fig. 5: Overall analysis improvements

Fig. 6: Improvements wrt dispatchers

Fig. 7: Improvements wrt priorities

VII. EVALUATIONS

We evaluate the proposed approach against the extended version of the existing method [9], presented in Section VI-B, referred to as the existing method in the subsequent text. Specifically, we perform the comparison in terms of the analysis and the runtime performance. Since platforms with prioritypreemptive wormhole-switched NoCs are still not available, the simulations were performed on the extended version of the SPARTS [33] simulator. For each experiment we randomly generate application-sets and map them on the platform. Subsequently, for both methods we (i) compute the upperbound on the worst-case communication delay (WCCD) of each application and (ii) perform the simulations to obtain the observed WCCD of each application. Then, we compare the results, and investigate how the trends change with different application parameters, e.g. number of dispatchers, priorities, employed agreement protocols. The analysis and simulation parameters are given in Table I. An asterisk sign denotes a randomly generated value, assuming a uniform distribution.

TABLE I: Analysis parameters

Platform size	10×10
Application-set size	200
Router arbitration + switch latency - l_{sw}	3 cycles
Router transfer latency - l_t	1 cycle
Rerouting operation latency - l_r	10.000 cycles
Link bandwidth	16B
Protocol message size	1kB
Context size = Inter-Application message size	[1-128]* kB
Minimum inter-arrival period of applications	[30-1000]* msec
Probability of inter-application communication	5 %
Simulated time	100 sec

Experiment 1. Overall Analysis Improvements

In this experiment we conducted the overall analytic comparison of the proposed method and the existing one. Specifically, each application had $[2-10]^*$ dispatchers, and was randomly mapped on the grid with arbitrary (rectangular or line-like) shape, assuming dispatcher placement constraints (Constraint 1). Half of the applications executed the List protocol, and the other half Hybrid. Then, the analytic upperbound on WCCD was obtained for each application of the application-set with both approaches. Finally, we compared the obtained values. The process was repeated for 1000 application-sets.

Figure 5 shows the improvements of the proposed approach over the existing one. In only 9.63% of the cases the proposed approach rendered worse results. This is further investigated in Experiment 3. In the remaining 90.37% scenarios the

proposed approach reports improvements. The improvements were expressed in percentages, using the following metric: $imp = \frac{del_{old}(a) - del_{new}(a)}{del_{old}(a)}, \text{ where } del_{old}(a) \text{ corresponds to the upper-bound on } WCCD \text{ of an application } a \text{ obtained by the existing method, and } del_{new}(a) \text{ with the proposed method. In more than half of the cases the improvements are greater than 50%, which means that a derived upper-bound is at most half the one obtained with the existing approach. Finally, in 5.46% of the scenarios the improvements are above 90%, corresponding to an estimate that is at most$ **one-tenth** $of the value against which it is compared! That is, <math display="block">del_{new}(a) \leq \frac{1}{10} del_{old}(a).$

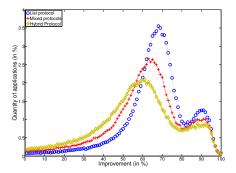
Experiment 2. Analysis Improvements wrt Dispatchers

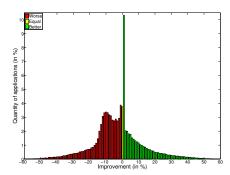
In order to test the scalability of the proposed approach we varied the number of dispatchers $|d|=[2-x]^*$ constituting each application. The parameter x was varied in the range $x \in \{4,6,8,10,12,14\}$ (see the legend of Figure 6). Assuming a certain range (e.g. $|d|=[2-4]^*$), we generated and randomly mapped all applications. Then, for each application we obtained analytic WCCD upper-bound estimates, computed by (i) the existing method and (ii) the proposed method, compared them with the same metric, and repeated that for 1000 application-sets. The process was performed for every range (Figure 6).

As the number of dispatchers increases, so does the category with worse results and the categories with smaller improvements (until 60%), while other categories with more significant improvements (above 60%) decrease. The explanation is twofold. First, assuming the proposed approach, more dispatchers cause more reroutings. This in turn causes more significant rerouting interferences, which have an impact on the derived worst-case delays. Conversely, in the existing approach reroutings do not occur. Additionally, more dispatchers cause more messages, leading to more significant and complex message interfering scenarios. In such cases, making an assumption that every higher priority message existing within the network will indeed cause interference might not be too pessimistic. Thus, as the number of dispatchers increases, the existing method becomes less pessimistic and hence improvements caused by the proposed method over it are slowly decreasing.

Experiment 3. Analysis Improvements wrt Priorities

In this experiment we investigated how the improvements of the proposed method over the existing one change with application priorities. This experiment also helped us to recognise





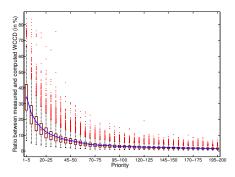


Fig. 8: Improvements wrt protocols

Fig. 9: Overall performance comparison

Fig. 10: Analysis tightness

and investigate the cases where the proposed method underperformed. The values obtained in Experiment 1 were used, but the comparison between the approaches was additionally performed per-priority and depicted in Figure 7.

Improvements were measured with the same metric, while the cases where the proposed approach underperformed were presented with the transposed metric: imp = $\frac{del_{old}(a) - del_{new}(a)}{dol}$. It is evident that the proposed approach performs worse for applications with higher priorities (smaller numbers on the x-axis of Figure 7). As these applications do not suffer significant interference, the additional delay in the proposed approach is caused by reroutings and longer message distances (i.e. traversal constraints expressed by Constraint 2). As priorities decrease, the interference becomes a more dominant term in the derived WCCD upper-bounds, hence the penalty of the proposed approach slowly decays. At the priority level 15, both the approaches provide similar results. Any additional decrease in the priority favours more the proposed method, where improvements report logarithmic growth. On the far right end of the domain the average improvements of the proposed approach asymptotically converge towards 70%.

Experiment 4. Analyis Improvements wrt Protocols

In this experiment we compared the approaches with an emphasis on the employed agreement protocols. The comparison was performed for 3 different scenarios: (i) all applications are utilising the List protocol, (ii) all applications are utilising the Hybrid protocol, (iii) half of the applications are utilising each protocol. For each application the WCCD upper-bound estimate were obtained with both approaches. The process was repeated for 1000 application-sets. The results are plotted in Figure 8, where the improvements of the proposed approach over the existing one were expressed by the same metric used in the previous experiments.

The conclusions are similar to that of Experiment 2. The Hybrid protocol involves more messages, which in the proposed approach induce more reroutings. Hence, the biggest improvements are reported for the List protocol, then for mixed protocols, while the least improvements occur with the Hybrid protocol. Additionally, as the improvement metric is based on the ratio, similarly to the logarithmic scale, each additional improvement percentage covers larger part of the domain; e.g. for improvements of 49-50% the following holds: $del_{old}(a) \in \{1.96 \times del_{new}(a), 2 \times del_{new}(a)\}$, while for improvements of 89-90% the following holds: $del_{old}(a) \in \{9.09 \times del_{new}(a), 10 \times del_{new}(a)\}$. Due to that fact, improvement ranges around 90% cover large parts of the

domain, and cause small local maximums, visible in Figure 8.

Experiment 5. Overall Performance Comparison

The purpose of this experiment is to compare the runtime performance of the proposed and the existing approach. In order to do that, we used application-sets generated for Experiment 1 and performed the simulations. The execution of each application-set was simulated for 100 seconds with both approaches. Within each approach WCCD was measured for each application. Consequently, the obtained values were compared and the results are presented in Figure 9.

Since the proposed approach induces longer message distances and employs the rerouting mechanism, we intuitively expected that it will systematically suffer a significant runtime performance penalty, when compared to the existing method. However, we reached the surprising conclusion: **not only was** the penalty negligible in almost all cases, but also for almost 40% of the scenarios the proposed approach outperformed the existing one! We interpret these unexpected findings in the following way. Unpredictable message paths may lead to corner cases where a traffic becomes heavily concentrated in certain links of the grid, resulting in significant contentions and (almost) unbounded interference delays. This coincides with the conclusions from our previous work [29], that network interference indeed is the most dominant component in WCCD. and also reflects the importance of having predictable messagepaths. Both these facts additionally validate the contributions of this work. This further implies that the overall efficiency of the system is heavily dependent on the application-mapping process, and we see this area as a potential topic for the future work.

Experiment 6. Analysis Tightness

Finally, in this experiment we tested the tightness of the proposed approach. Specifically, for each application of the application-set we compared the analytically computed upper-bound estimate on its WCCD against the WCCD value obtained via simulations. The input parameters are identical to that of Experiment 1 and Experiment 5. In order to get a better insight into the ratios between obtained values, we have grouped applications into categories, according to their priorities. Figure 10 illustrates the findings.

The highest observed ratio between the measured WCCD and the analytically computed upper-bound estimate was around 85%, suggesting that the analysis is correct and renders tight estimates. Furthermore, the results demonstrated an average ratio of almost 35% for applications with high

priorities (smaller numbers on the x-axis of Figure 10). As the priorities decreased, so did the ratio, which asymptotically converged towards 2% for applications with very low priorities. The explanation is as follows. As the priority decreases, the analysis considers more and more complex interference (worst-case) scenarios, which are less and less likely to occur during simulations. Given our findings, that the chance of suffering the worst-case scenario by lower-priority applications is small, and that in many practical scenarios their occasional missed deadlines are tolerable, several questions can be raised. For instance, is the worst-case analysis the right approach to treat the lower-priority applications? Or should some other (e.g. probabilistic) techniques be applied? We see these questions as starting points for our future work.

VIII. CONCLUSIONS AND FUTURE WORK

The Limited Migrative Model (LMM) is a promising step towards scalable and predictable many-cores, which are essential prerequisites for their integration in the real-time embedded domain. Assuming LMM, we analysed the worstcase communication delays of individual applications. First, we extended LMM to accommodate the inter-application traffic. Then, we enhanced the existing analysis [9], so as to make it applicable to the extended model. Finally, we proposed a novel approach which potentially "sacrifices" the performance in order to gain in predictability (i.e. determinism in message paths). We compared the methods both in terms of the analysis and the performance. The experiments show that the proposed approach not only renders tighter upper-bound estimates in more than 90\% of the cases, but also demonstrates a comparable runtime performance, which reflects the importance of having deterministic traffic routes.

The future work has already been discussed throughout the paper; we plan to develop an application mapping approach that will use the presented analysis, together with an LMM schedulability analysis (e.g. [31]), as feasibility tests. Additionally, providing an alternative (probabilistic?) method to treat lower-priority workload is a challenge with an increasing importance.

REFERENCES

- [1] M. Lundstrom, "Moore's law forever?" Science, 2003.
- Intel, Single-Chip-Cloud Computer, www.intel.com/content/dam/www/public/us/en/documents/ technology-briefs/intel-labs-single-chip-cloud-article.pdf.
- [3] Tilera, *TILE64*TM *Processor*, www.tilera.com/products/processors/TILEPro_Family.
- [4] L. Benini and G. De Micheli, "Networks on chips: a new soc paradigm," The Comp. J., vol. 35, no. 1, pp. 70 –78, jan 2002.
- [5] N. K. Kavaldjiev and G. J. M. Smit, "A survey of efficient on-chip communications for soc," in Symp. Emb. Syst., 2003.
- [6] L. M. Ni and P. K. McKinley, "A survey of wormhole routing techniques in direct networks," *The Comp. J.*, vol. 26, 1993.
- [7] A. Baumann, P. Barham, P.-E. Dagand, T. Harris, R. Isaacs, S. Peter, T. Roscoe, A. Schüpbach, and A. Singhania, "The multikernel: A new os architecture for scalable multicore systems," in SOSP, 2009.
- [8] R. Kumar, T. G. Mattson, G. Pokam, and R. van der Wijngaart, "The case for message passing on many-core chips," in *Multiprocessor System-on-Chip*. Springer, 2011, pp. 115–123.

- [9] B. Nikolić and S. M. Petters, "Towards network-on-chip agreement protocols," in 12th EMSOFT, 2012.
- [10] D. Wentzlaff and A. Agarwal, "Factored operating systems (fos): the case for a scalable operating system for multicores," SIGOPS Oper. Syst. Rev., 2009.
- [11] M. D. Y. Li and R. West, "Quest-v: A virtualized multikernel for high-confidence systems," Tech. Rep., http://www.cs.bu.edu/~richwest/quest. html.
- [12] C. L. Liu and J. W. Layland, "Scheduling algorithms for multiprogramming in a hard-real-time environment," J. ACM, 1973.
- [13] K. Bletsas and B. Andersson, "Preemption-light multiprocessor scheduling of sporadic tasks with high utilisation bound," in 30th RTSS, 2009.
- [14] S. Kato, N. Yamasaki, and Y. Ishikawa, "Semi-partitioned scheduling of sporadic task systems on multiprocessors," in 21st ECRTS, 2009.
- [15] T. P. Baker, "An analysis of fixed-priority schedulability on a multiprocessor," *Real-Time Syst. J.*, 2006.
- [16] S. Baruah and T. Baker, "Schedulability analysis of global edf," Real-Time Syst. J., 2008.
- [17] P. L. Holman, "On the implementation of pfair-scheduled multiprocessor systems," Ph.D. dissertation, The University of North Carolina at Chapel Hill, North Carolina, United States, 2004.
- [18] K. Goossens and A. Hansson, "The aethereal network on chip after ten years: Goals, evolution, lessons, and future," in 47th DAC, 2010.
- [19] B. Kim, J. Kim, S. Hong, and S. Lee, "A real-time communication method for wormhole switching networks," in 1998 Int. Conf. Parall. Processing, Aug 1998.
- [20] D. Dasari, B. Nikolić, V. Nelis, and S. M. Petters, "Noc contention analysis using a branch and prune algorithm," *Trans. Emb. Comput.* Syst., 2013.
- [21] T. Ferrandiz, F. Frances, and C. Fraboul, "A method of computation for worst-case delay analysis on spacewire networks," in *IEEE Int. Symp. Industrial Emb. Syst.*, 2009.
- [22] —, "Using network calculus to compute end-to-end delays in spacewire networks," SIGBED Rev., 2011.
- [23] Y. Qian, Z. Lu, and W. Dou, "Analysis of worst-case delay bounds for best-effort communication in wormhole networks on chip," in *Int. Symp. Netw.-on-Chip.* 2009.
- [24] W. Dally and C. Seitz, "Deadlock-free message routing in multiprocessor interconnection networks," *Trans. Computers*, 1987.
- [25] W. Dally, "Virtual-channel flow control," Trans. Parall. & Distr. Syst., vol. 3, no. 2, pp. 194 –205, Mar 1992.
- [26] H. Song, B. Kwon, and H. Yoon, "Throttle and preempt: a new flow control for real-time communications in wormhole networks," in 1997 Int. Conf. Parall. Processing, Aug 1997.
- [27] Z. Shi and A. Burns, "Real-time communication analysis for on-chip networks with wormhole switching," in *Int. Symp. Netw.-on-Chip*, 2008.
- [28] J. Hu and R. Marculescu, "Energy-aware mapping for tile-based noc architectures under performance constraints," in 8th ASPDAC, 2003.
- [29] B. Nikolić, H. I. Ali, S. M. Petters, and L. M. Pinho, "Are virtual channels the bottleneck of priority-aware wormhole-switched noc-based many-cores?" in 21th RTNS, 2013.
- [30] B. Nikolić, P. M. Yomsi, and S. M. Petters, "Worst-case memory traffic analysis for many-cores using a limited migrative model," in 19th RTCSA, 2013.
- [31] B. Nikolić, K. Bletsas, and S. M. Petters, "Priority assignment and application mapping for many-cores using a limited migrative model," Tech. Rep., available at: http://www.cister.isep.ipp.pt/people/Borislav+ Nikolic/publications/.
- [32] B. Nikolić, P. M. Yomsi, and S. M. Petters, "Worst-case communication delay analysis for many-cores using a limited migrative model," Tech. Rep., available at: http://www.cister.isep.ipp.pt/people/Borislav+Nikolic/publications/.
- [33] B. Nikolić, M. A. Awan, and S. M. Petters, "SPARTS: Simulator for power aware and real-time systems," in 8th IEEE ICESS, 2011.